

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("20020072893").PN.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:33
L2	19	reconfigurable adj cache	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:42
L3	4759	optim\$6 adj configura\$6	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:33
L4	2	2 and 3	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:34
L5	18339	FPGA or (field adj programmable adj gate adj array\$)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:43
L6	1783	optimal adj configura\$6	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:43
L7	8	5 same 6	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:43
L8	17	3 same 5	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:43
L9	1	2 and 5	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:44
L10	2	8 and cache	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:45

L11	1574	(711/118).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:45
L12	1773	(711/154).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:45
L13	145	(711/166).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:45
L14	1579	(711/170).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/04/12 13:45